

# FI4110G-VB TO220F Datasheet N-Channel 100 V (D-S) 175 °C MOSFET

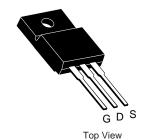
PRODUCT SUMMARY	
V <sub>DS</sub> (V)	100
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.0038
I <sub>D</sub> (A)	120
Configuration	Single

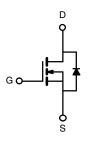
### **FEATURES**

- TrenchFET® Power MOSFET
- Package with Low Thermal Resistance
- $\bullet$  100 %  $R_{g}$  and UIS Tested









N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (	$\Gamma_{\rm C}$ = 25 °C, unles	ss otherwise noted)		
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		$V_{DS}$	100	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	V
Continuous Drain Current	T <sub>C</sub> = 25 °C <sup>a</sup>	1	120	
Continuous Drain Current	T <sub>C</sub> = 125 °C	l <sub>D</sub>	102	
Continuous Source Current (Diode Conduction) <sup>a</sup>		Is	120	Α
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	480	
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	73	
Single Pulse Avalanche Energy	L = 0.1 IIII	E <sub>AS</sub>	266	mJ
Mariana Damar Diagia atianh	T <sub>C</sub> = 25 °C	0	84	W
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 125 °C	$P_D$	35	VV
Operating Junction and Storage Temperature Ra	ange	T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient	PCB Mount <sup>c</sup>	$R_{thJA}$	40	°C/W	
Junction-to-Case (Drain)		$R_{thJC}$	0.6		

#### Notes

- a. Package limited.
- b. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.

服务热线:400-655-8788

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PARAMETER	SYMBOL	TES	T CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static					l l		
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0$ , $I_D = 250 \mu A$		100	-	-	V
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	= V <sub>GS</sub> , I <sub>D</sub> = 250 μA	2.5	3.0	3.5	, v
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> =	$0 \text{ V}, \text{ V}_{GS} = \pm 20 \text{ V}$	=	-	± 100	nA
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 100 V	=	-	1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{GS} = 0 V$	V <sub>DS</sub> = 100 V, T <sub>J</sub> = 125 °C	-	-	50	μA
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 100 V, T <sub>J</sub> = 175 °C	=	-	500	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	$V_{DS} \ge 5 V$	120	-	-	Α
Drain-Source On-State Resistance <sup>a</sup>		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 20 A	-	0.0038	-	
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C	-	0.0064	-	Ω
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 20 A, T <sub>J</sub> = 175 °C	=	0.0080	-	
Forward Transconductance <sup>b</sup>	9fs	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 20 A		-	82	-	S
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			-	5780	7230	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$	$V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	-	3070	3840	pF
Reverse Transfer Capacitance	C <sub>rss</sub>	1		-	305	385	
Total Gate Charge <sup>c</sup>	Qg			-	125	190	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	V <sub>GS</sub> = 10 V	V <sub>DS</sub> = 50 V, I <sub>D</sub> = 70 A	-	28	-	nC
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$	1		-	46	-	
Gate Resistance	$R_g$	f = 1 MHz		1.6	3.3	5	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>				16	25	ns
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = 50 V, $R_L$ = 0.7 $\Omega$ $I_D$ $\cong$ 70 A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$		-	110	165	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	40	60	
Fall Time <sup>c</sup>	t <sub>f</sub>			=	12	20	
Source-Drain Diode Ratings and Chara	acteristics <sup>b</sup>						
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	480	Α
Forward Voltage	$V_{SD}$	I <sub>F</sub> = 100 A, V <sub>GS</sub> = 0		-	0.9	1.5	V

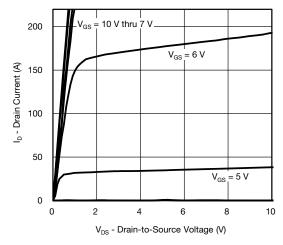
#### Notes

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

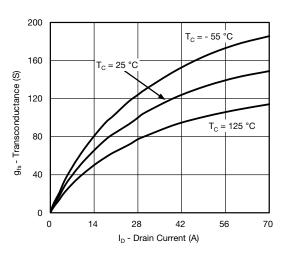
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



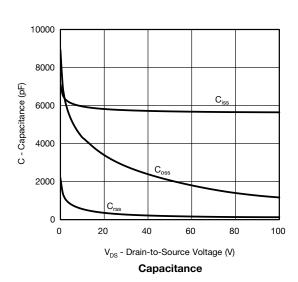
# TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)

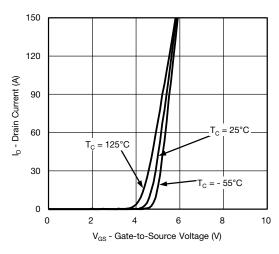


#### **Output Characteristics**

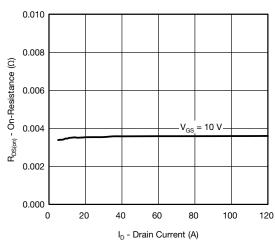


#### Transconductance

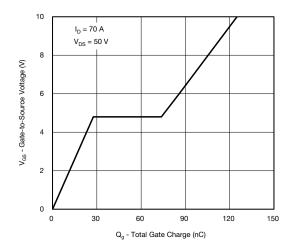




#### **Transfer Characteristics**



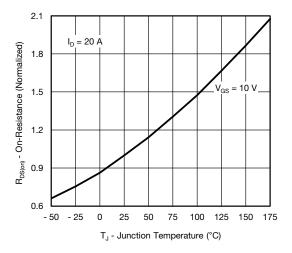
On-Resistance vs. Drain Current



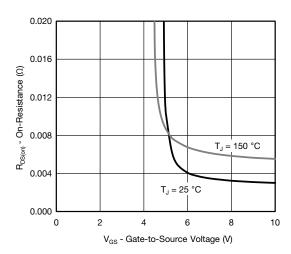
Gate Charge



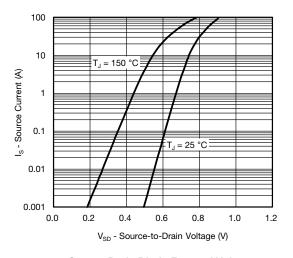
# **TYPICAL CHARACTERISTICS** ( $T_A = 25 \, ^{\circ}\text{C}$ , unless otherwise noted)



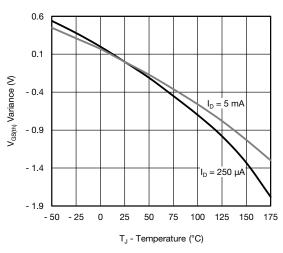
#### On-Resistance vs. Junction Temperature



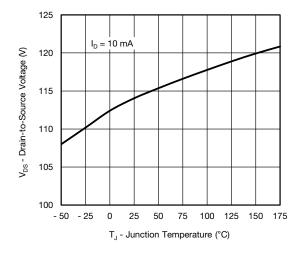
On-Resistance vs. Gate-to-Source Voltage



#### **Source Drain Diode Forward Voltage**



Threshold Voltage

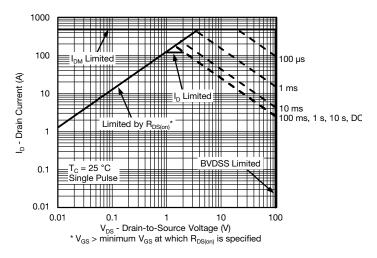


Drain Source Breakdown vs. Junction Temperature

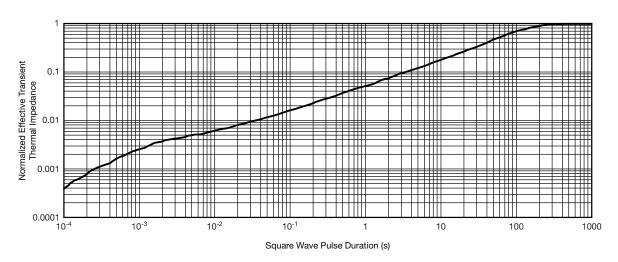


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## **THERMAL RATINGS** ( $T_A = 25$ °C, unless otherwise noted)



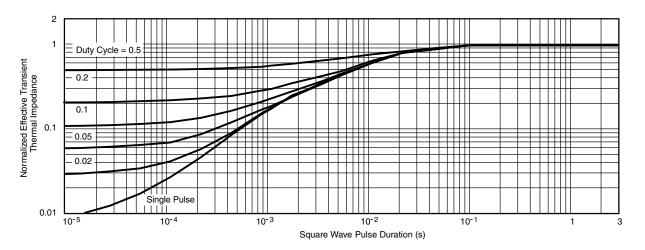
#### Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



### **THERMAL RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



#### Normalized Thermal Transient Impedance, Junction-to-Case

#### Note

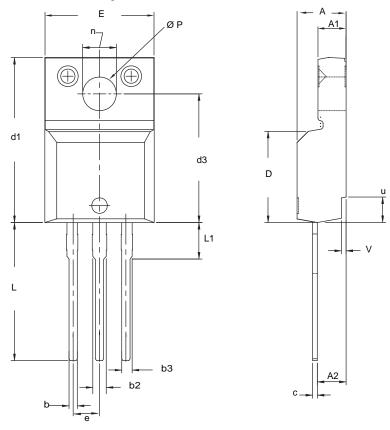
- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)

can widely vary depending on actual application parameters and operating conditions.

- Normalized Transient Thermal Impedance Junction to Case (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities



### **TO-220 FULLPAK (HIGH VOLTAGE)**



DIM.	MILLIN	METERS	INCHES		
	MIN.	MAX.	MIN.	MAX.	
Α	4.570	4.830	0.180	0.190	
A1	2.570	2.830	0.101	0.111	
A2	2.510	2.850	0.099	0.112	
b	0.622	0.890	0.024	0.035	
b2	1.229	1.400	0.048	0.055	
b3	1.229	1.400	0.048	0.055	
С	0.440	0.629	0.017	0.025	
D	8.650	9.800	0.341	0.386	
d1	15.88	16.120	0.622	0.635	
d3	12.300	12.920	0.484	0.509	
E	10.360	10.630	0.408	0.419	
е	2.54	BSC	0.100 BSC		
L	13.200	13.730	0.520	0.541	
L1	3.100	3.500	0.122	0.138	
n	6.050	6.150	0.238	0.242	
ØΡ	3.050	3.450	0.120	0.136	
u	2.400	2.500	0.094	0.098	
٧	0.400	0.500	0.016	0.020	

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- To be used only for process drawing.
  These dimensions apply to all TO-220, FULLPAK leadframe versions 3 leads.
  All critical dimensions should C meet C<sub>pk</sub> > 1.33.
  All dimensions include burrs and plating thickness.
  No chipping or package damage.



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